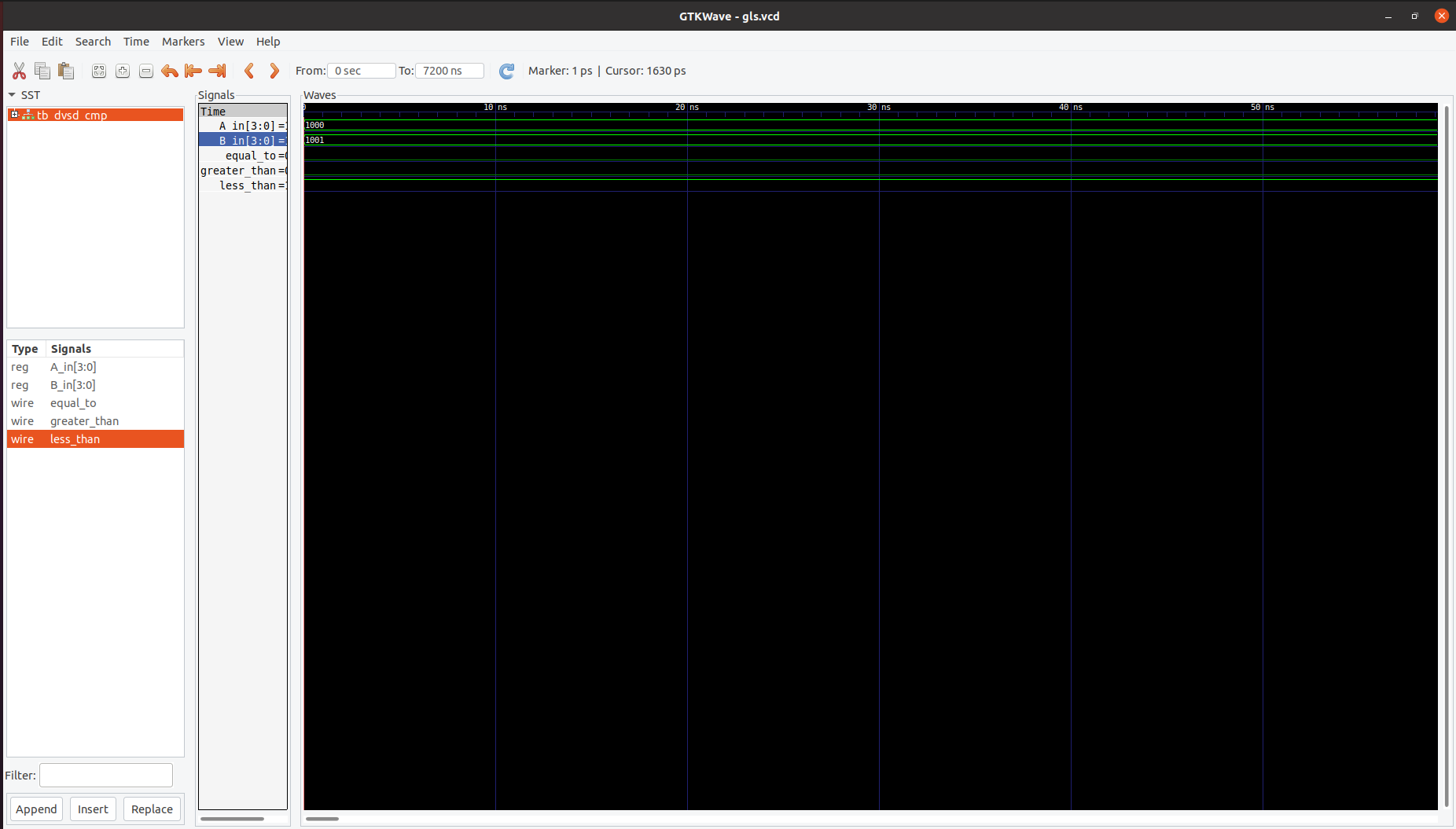
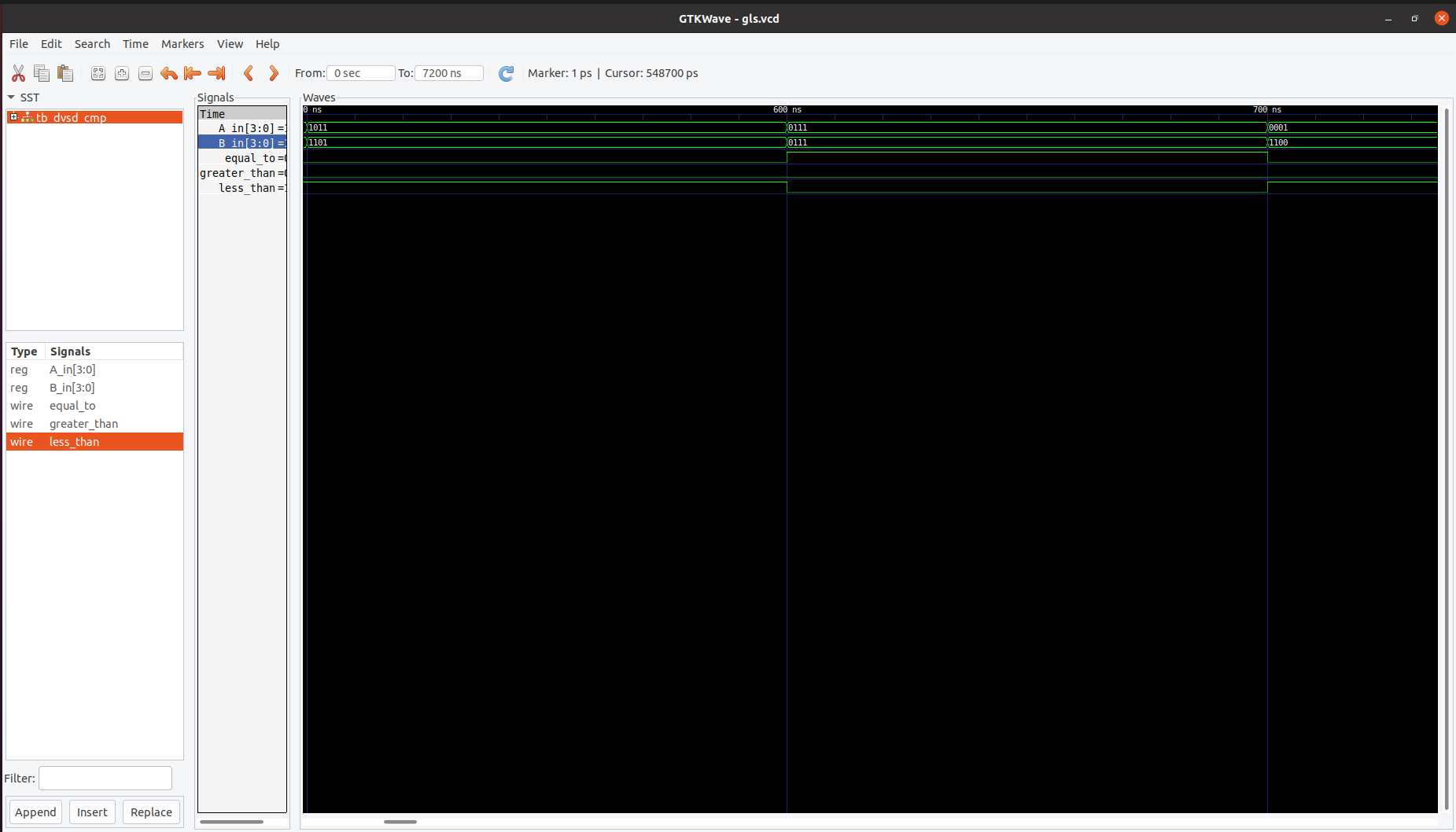
**Post –Layout Simulation of**

**4 Bit Magnitude Comparator**





V**OL**

V**oh**

